

FIG. 1
PRIOR ART

2/15

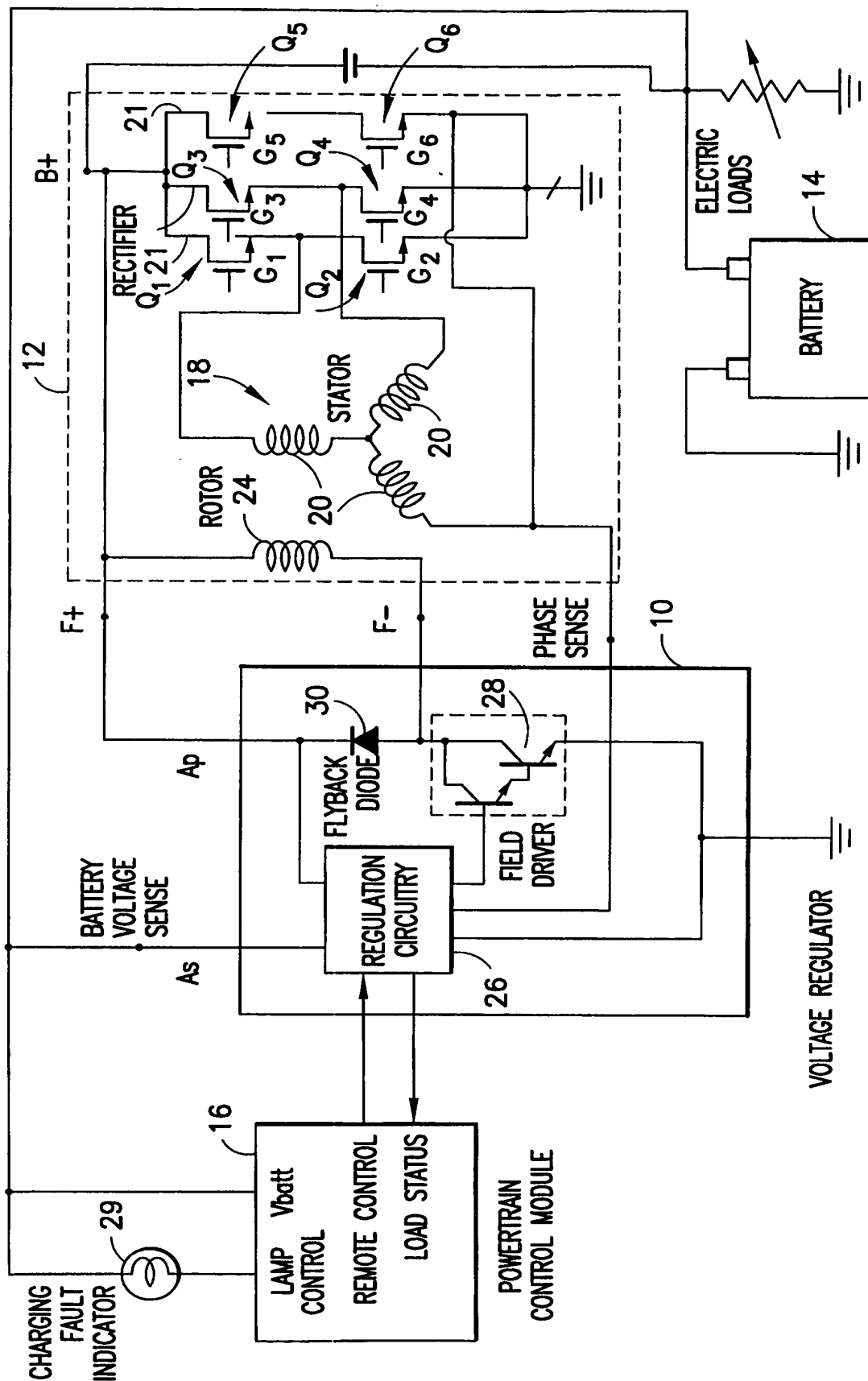


FIG.2

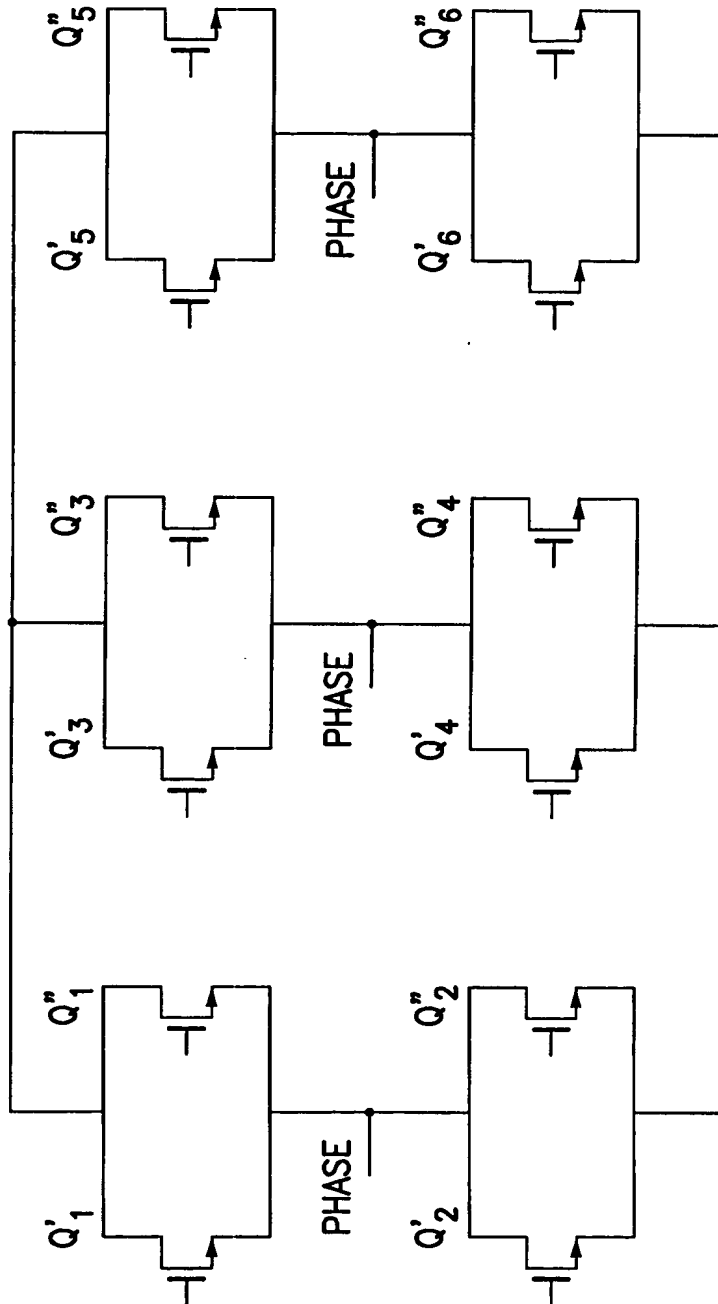


FIG.3

4/15

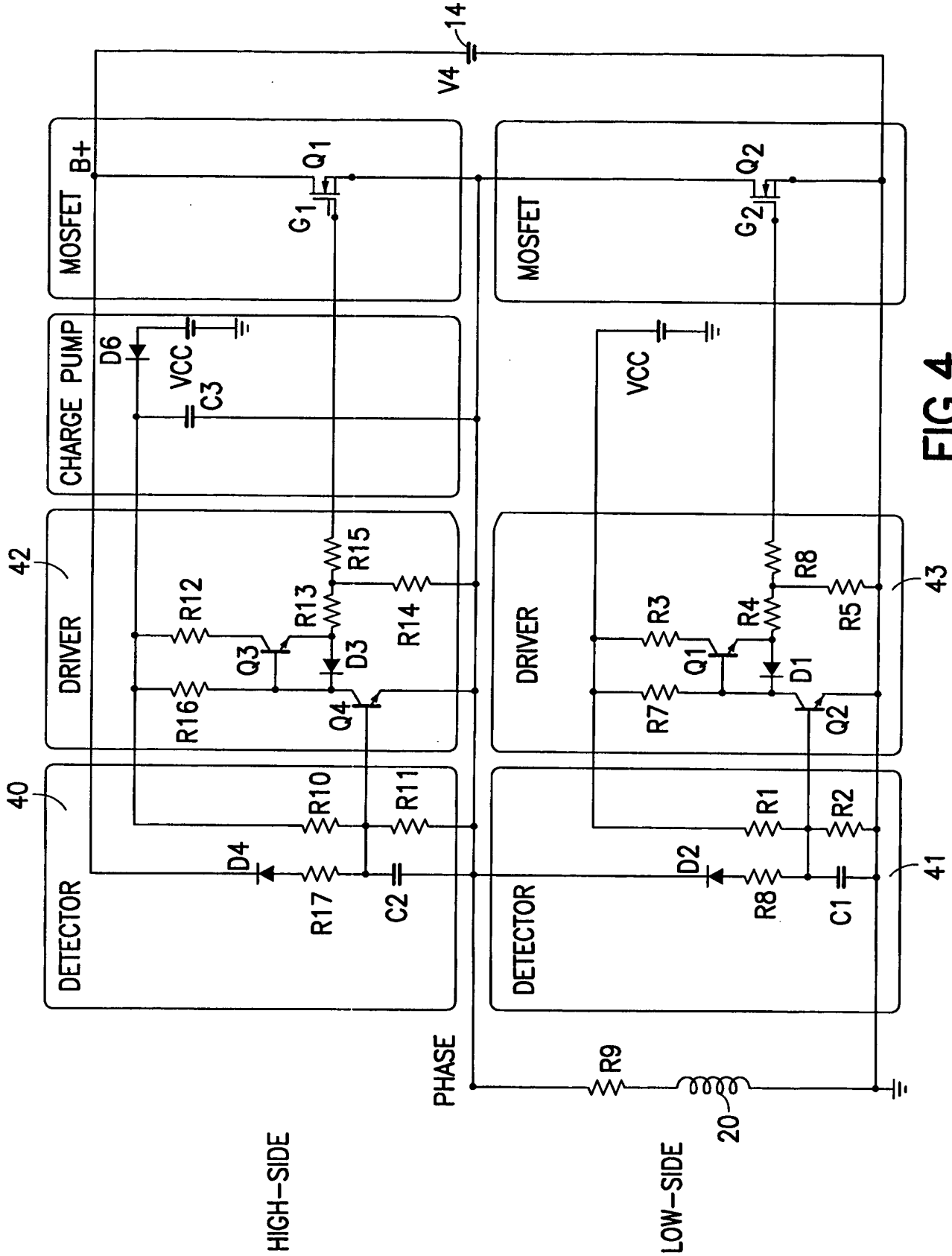


FIG. 4

GATE VOLTAGE, PHASE VOLTAGE, LOW CURRENT

CIRCUIT2.CIR Temperature=27

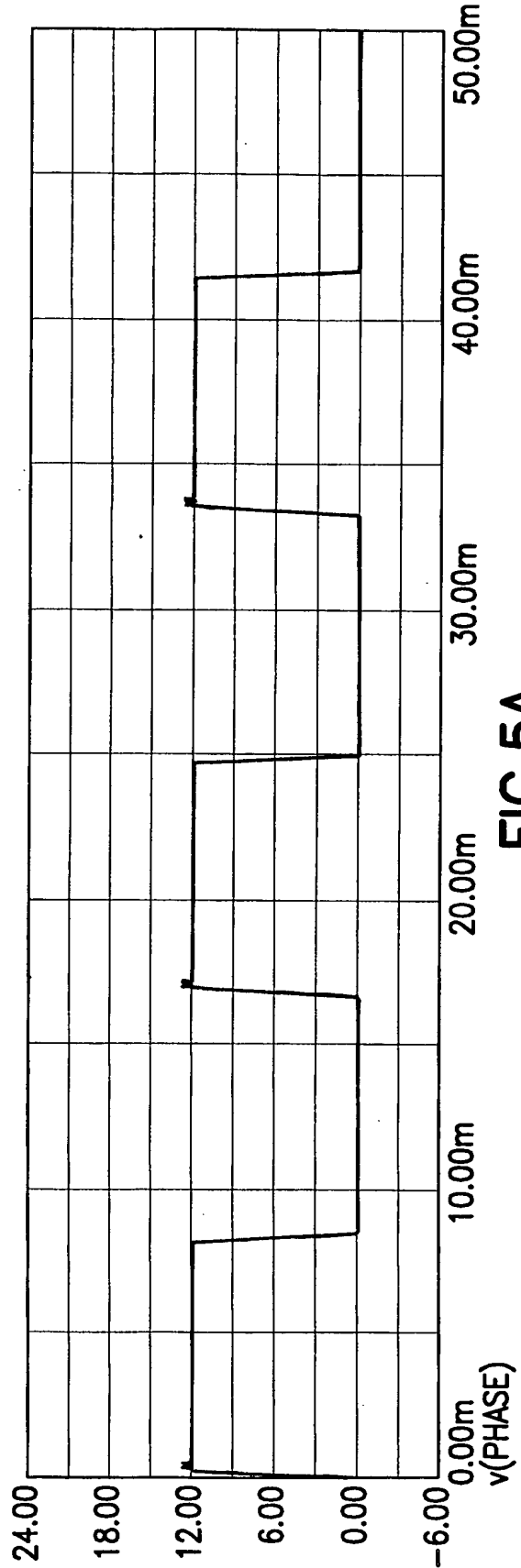
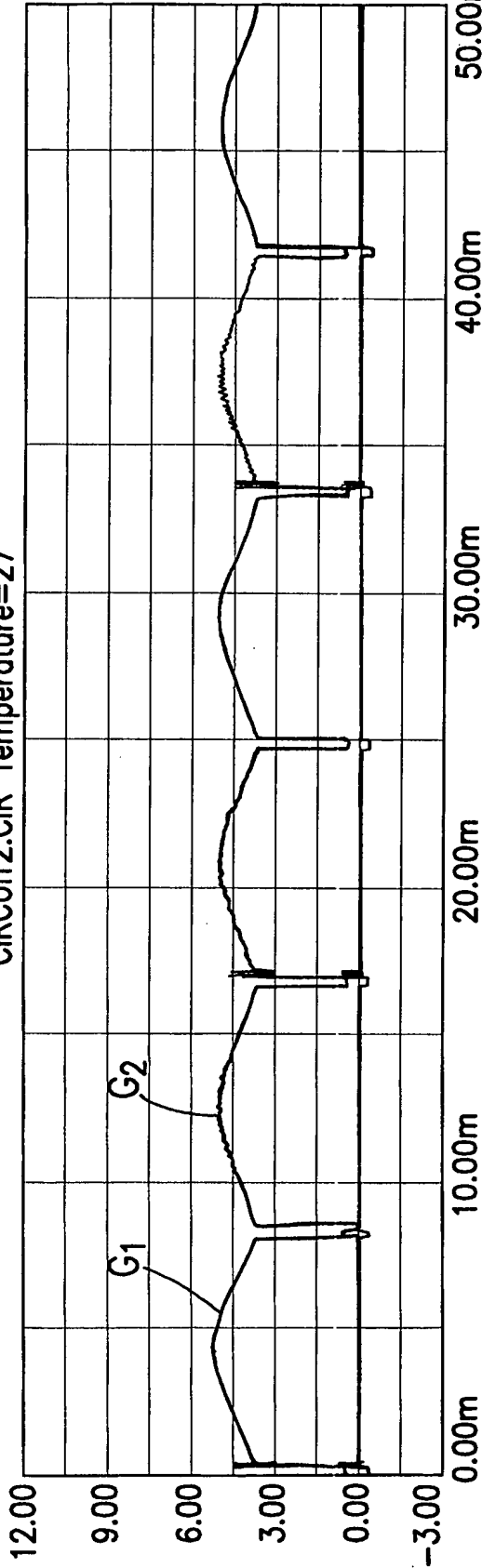


FIG.5A

6/15

GATE VOLTAGE, PHASE VOLTAGE, MEDIUM CURRENT

CIRCUIT2.CIR Temperature=27

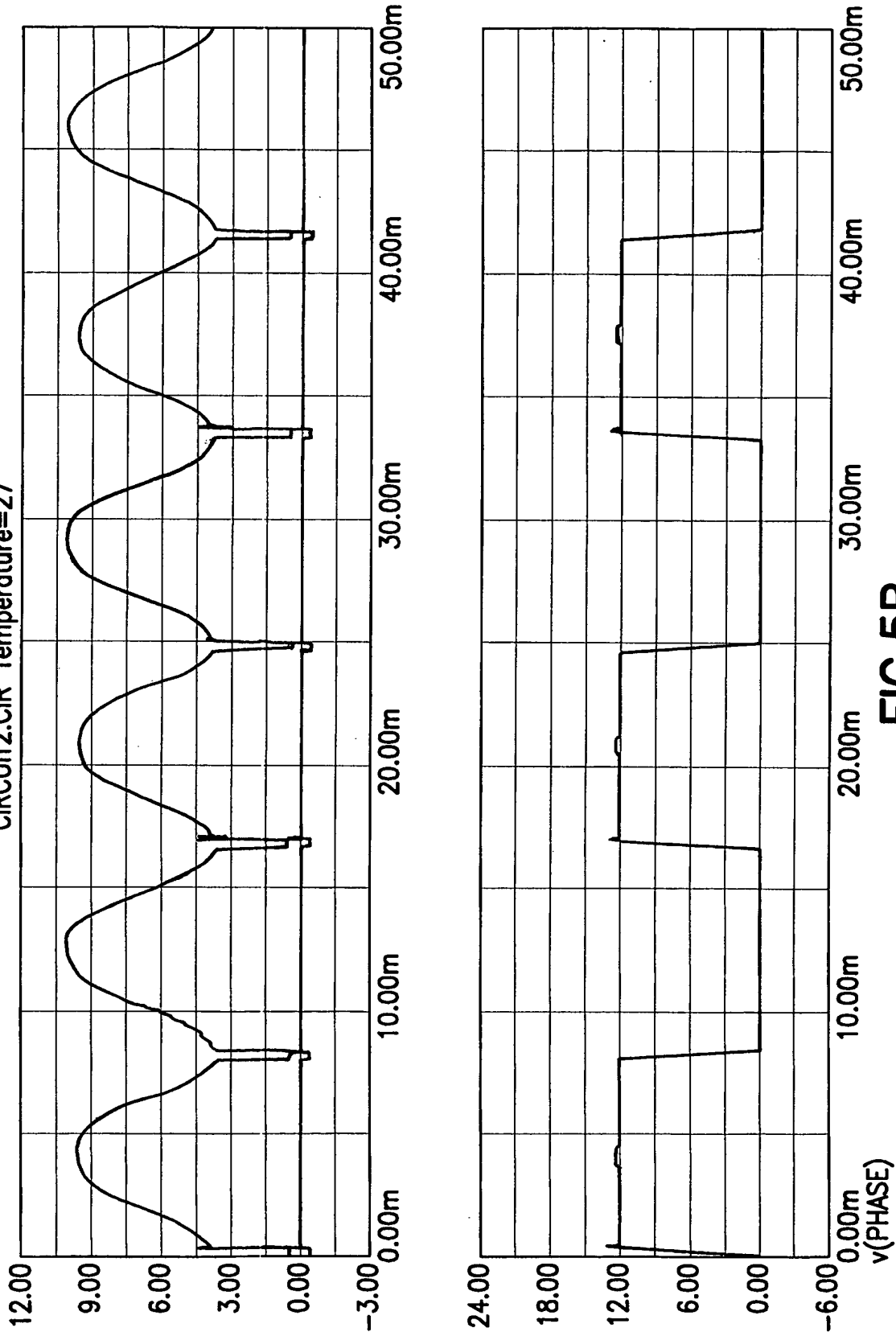
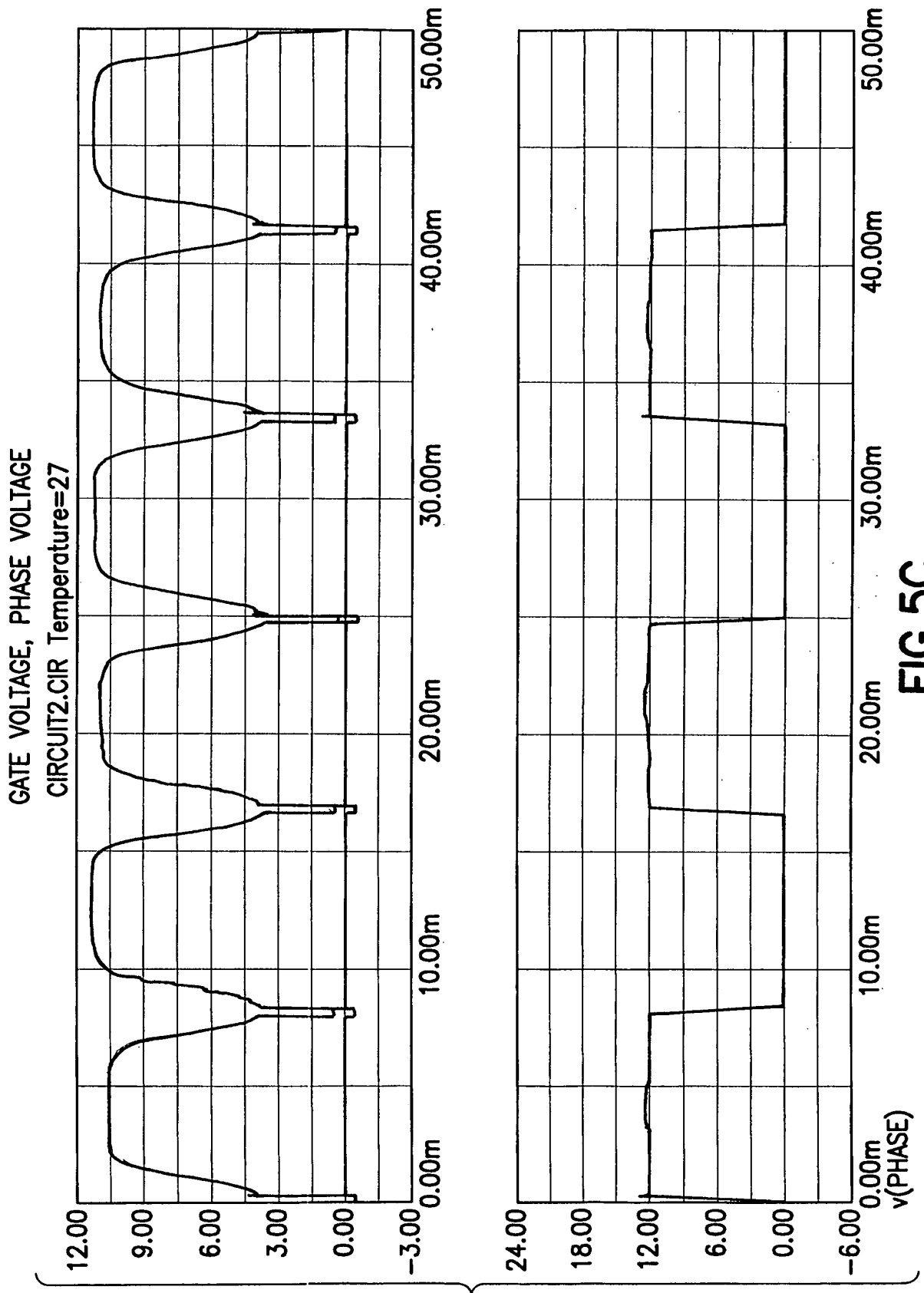


FIG.5B

7/15



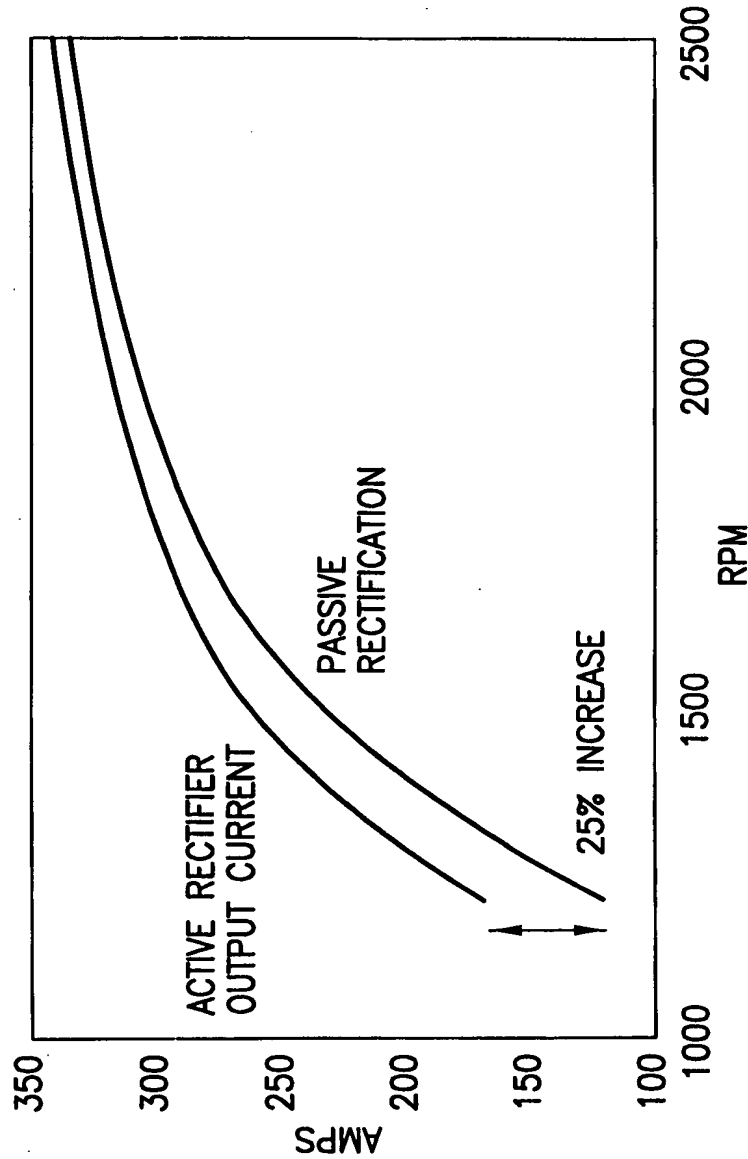


FIG. 6

9/15

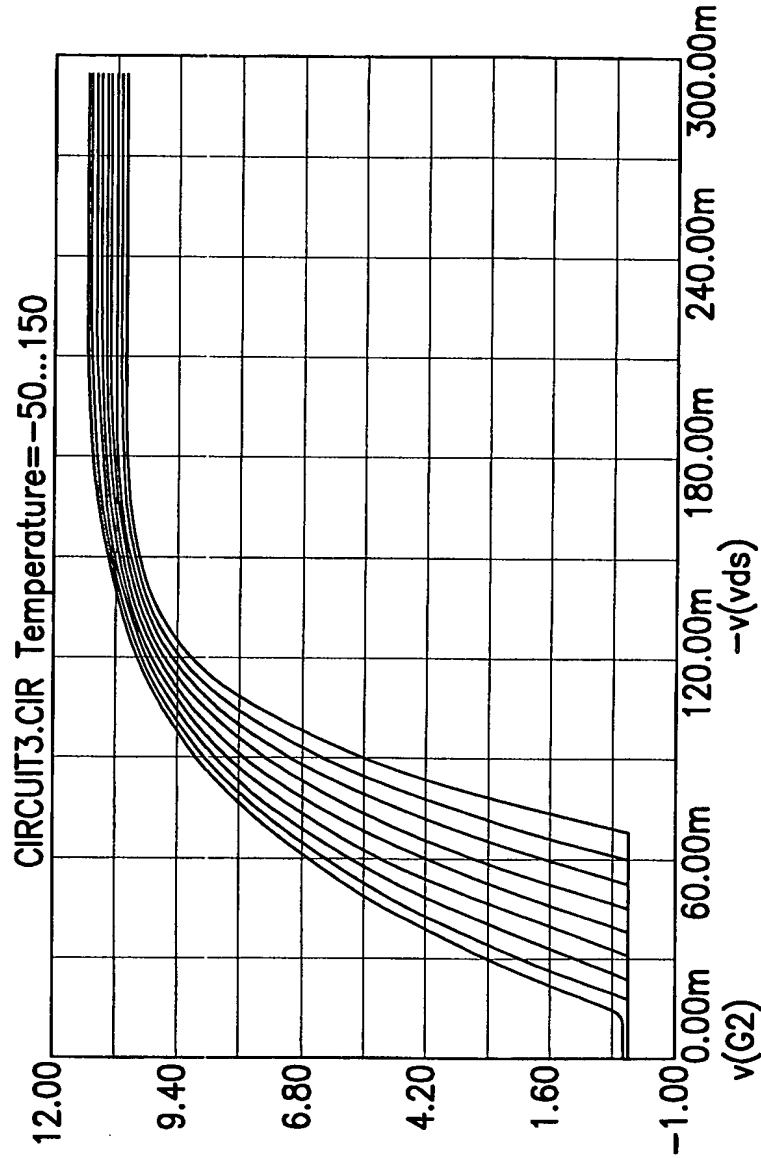


FIG.7

10/15

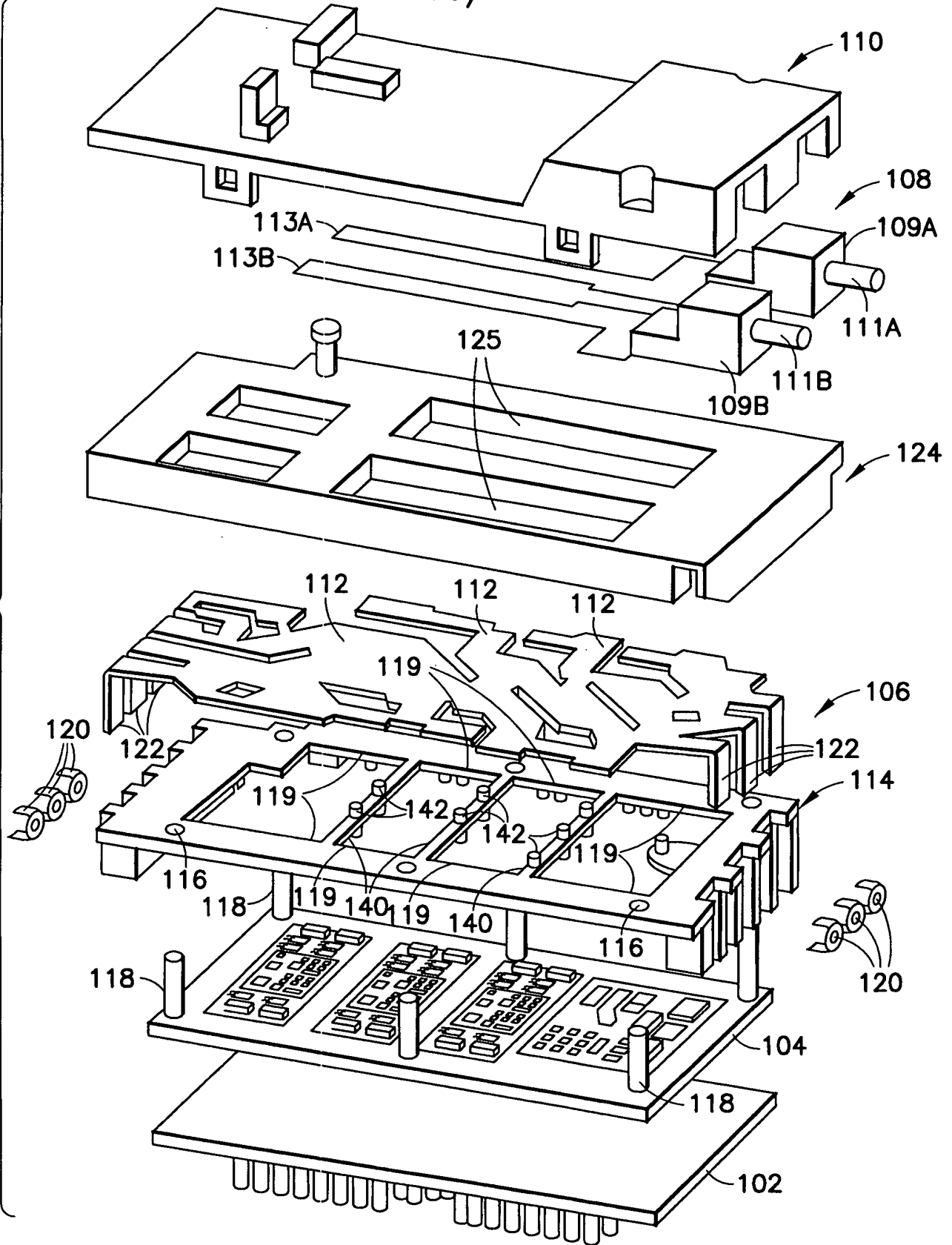


FIG.8

11/15

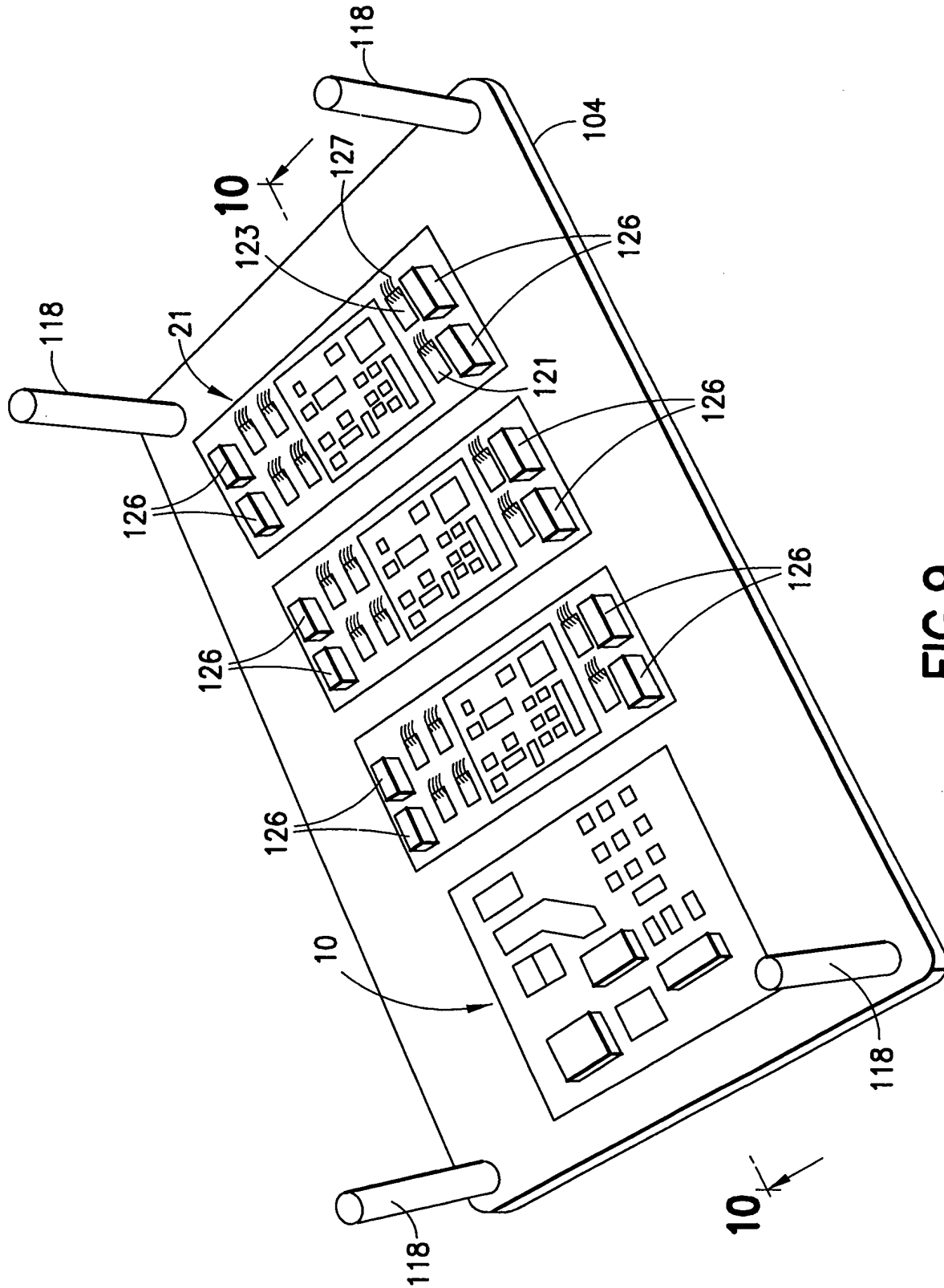


FIG. 9

12/15

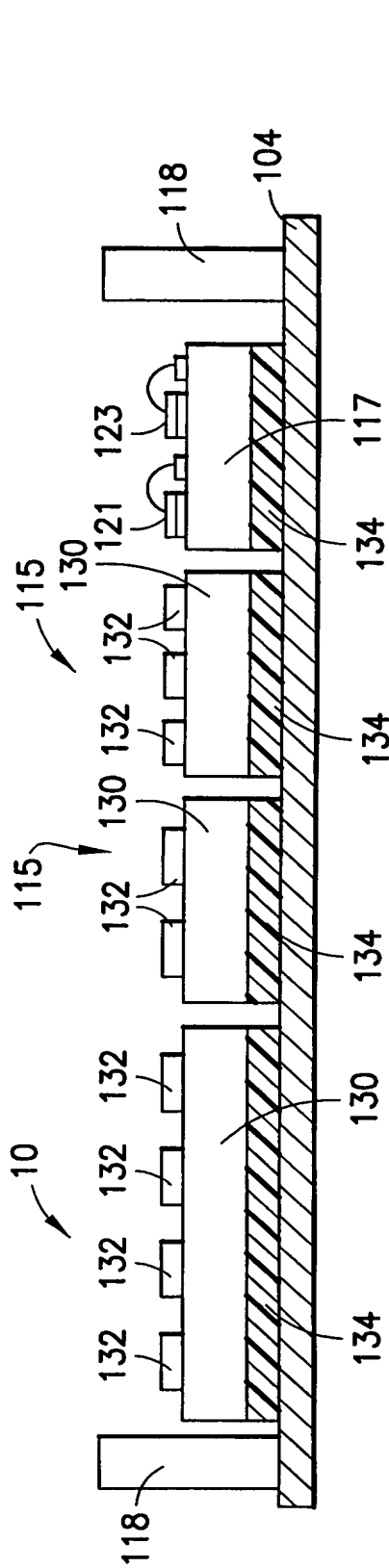


FIG. 10

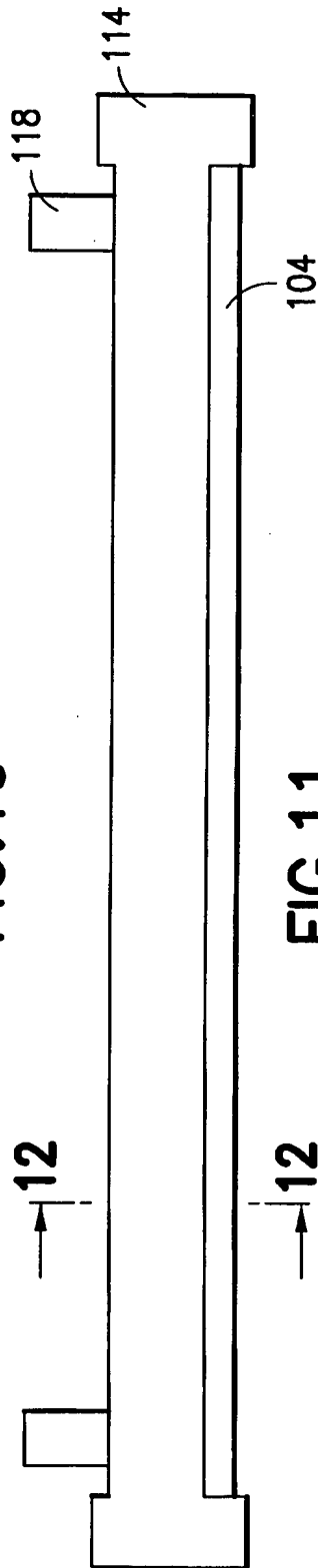


FIG. 11

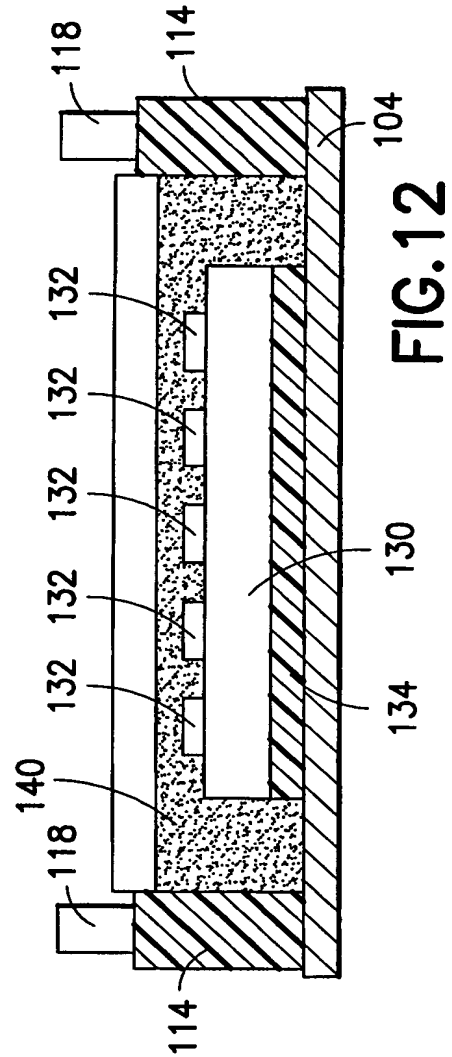


FIG. 12

13/15

BEST AVAILABLE COPY

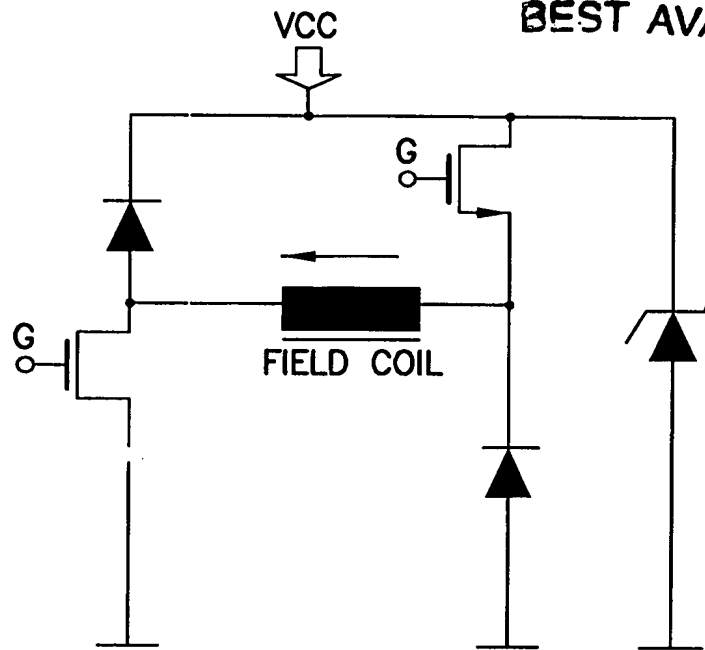


FIG.13A

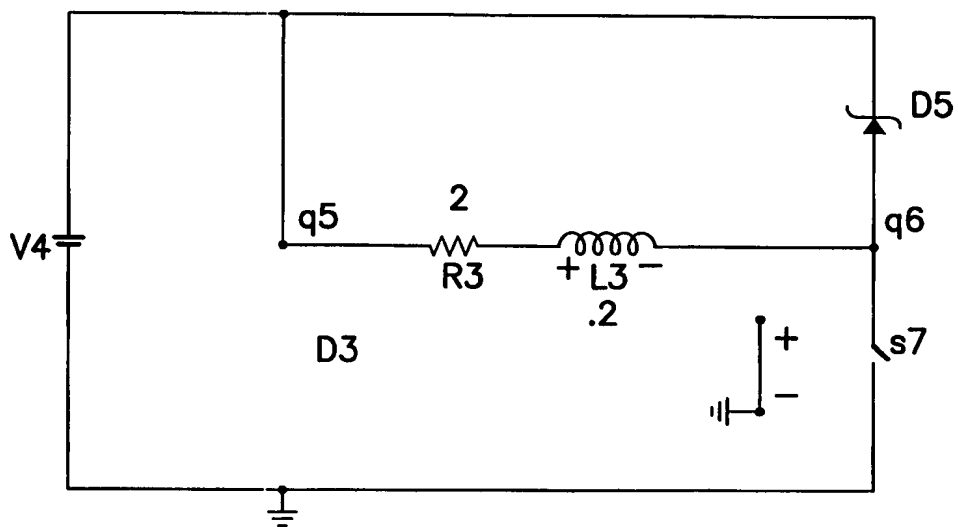


FIG.13B

14/15

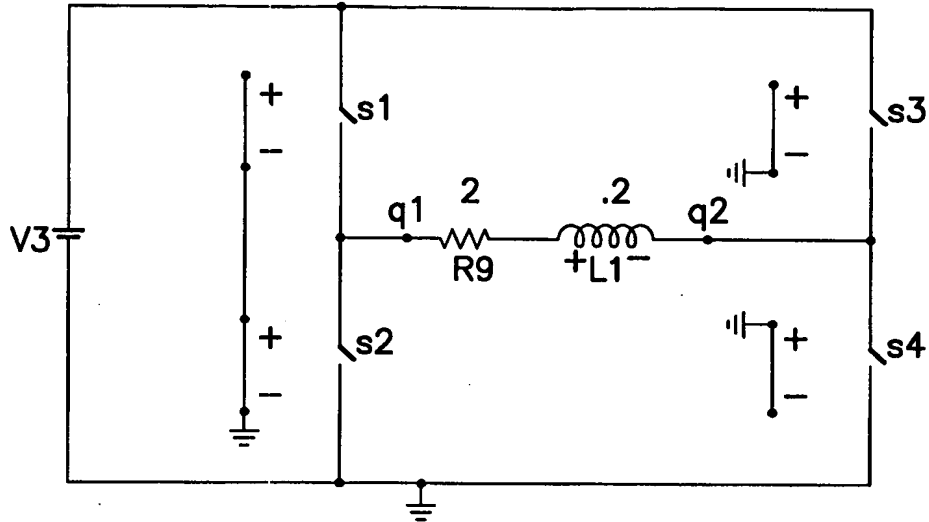


FIG. 13C

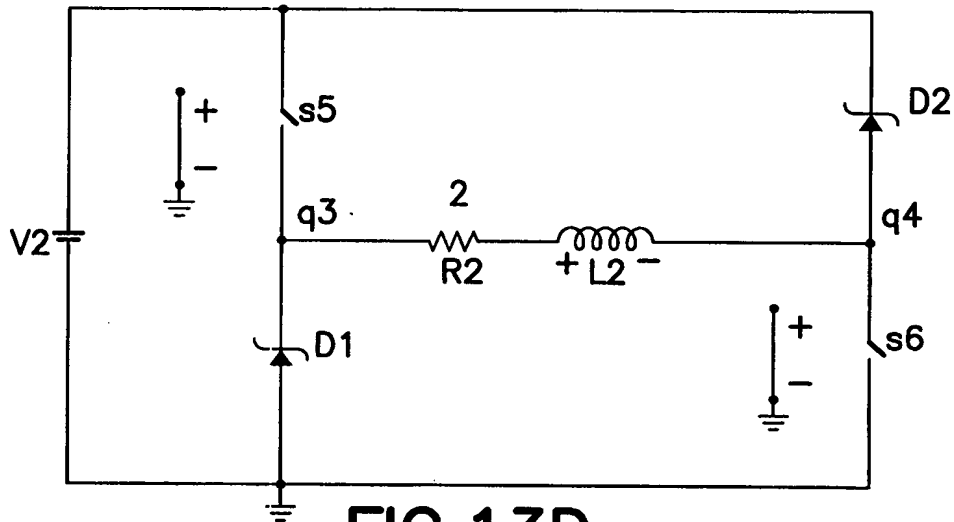


FIG. 13D

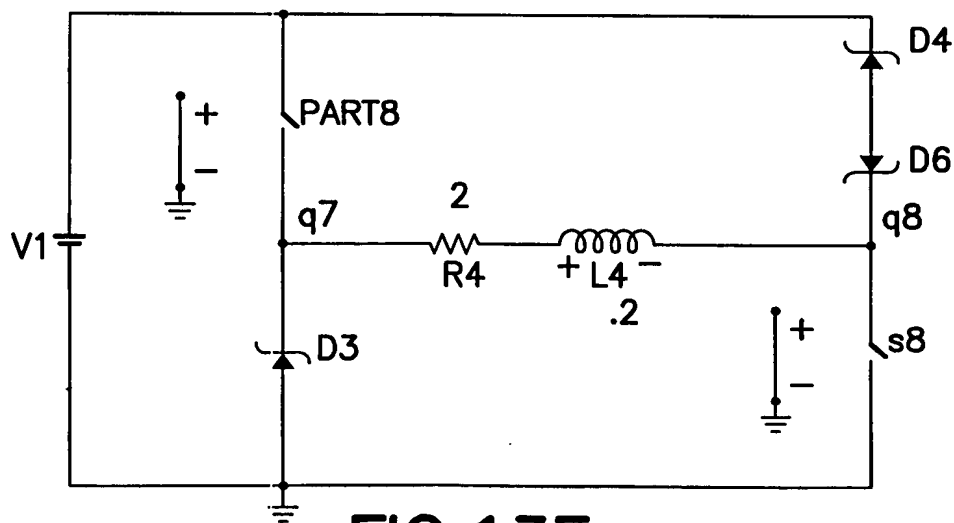


FIG. 13E

15/15

BEST AVAILABLE COPY

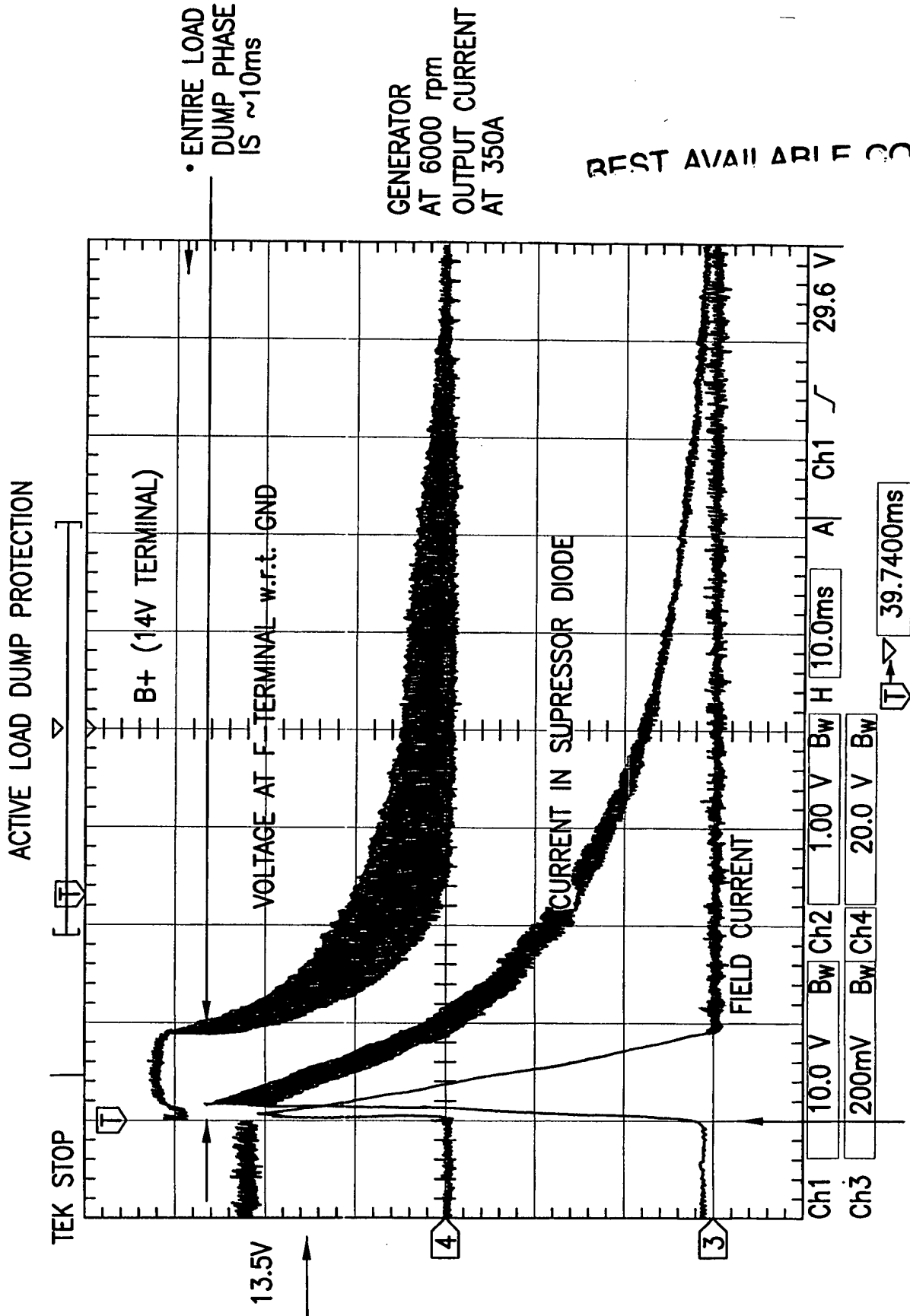


FIG.14